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PATENT & TRADEMARK OFFICE

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE**TRANSMITTAL OF FORMAL
DRAWINGS**Docket Number:
2207/12116Application Number
10/016,793Filing Date
October 26, 2001Examiner
Not yet assignedArt Unit
2838Invention Title
**SILICON INTERPOSER-BASED HYBRID
VOLTAGE REGULATOR SYSTEM FOR VLSI
DEVICES**Inventor(s)
NAIR, et al.

Address to:

Commissioner for Patents
Washington D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Box Missing Parts, Washington, D.C. 20231 on

Date: 6/27/02Signature: Howard Grossman

We are enclosing herewith a complete set of formal drawings for the above-identified application.

Dated: 6/27/02Howard Grossman
Howard I. Grossman, Reg. No. 48,673

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